



# JSKD/JSMD/JSND240

# **Description**

- 1) A package of series of two diodes.
- 2) With high thermal conductivity DBC as the insulation.
- 3) Welding by vacuum welding technology, which provide high reliability.



# **Typical Application**

AC converter, inverter and DC motor.

## **Absolute Maximum Ratings** (Packaged into modules, unless otherwise specified, Tcase=25℃)

Parameter	Test Conditions	Symbol	Values				
			12	16	18	20	Unit
Operating junction temperature range		Tj	-40-150				$^{\circ}$
Storage temperature range		T <sub>stg</sub>	-40-125				$^{\circ}$
Repetitive peak reverse voltage	T <sub>j</sub> =25℃	V <sub>RRM</sub>	1200	1600	1800	2000	V
Non-repetitive peak reverse voltage	T <sub>j</sub> =25℃	V <sub>RSM</sub>	1300	1700	1900	2100	V
Average forward current	Tc=100°C	I <sub>F(AV)</sub>	240				А
Peak forward surge current	t <sub>P</sub> =10ms, sin180°,	I <sub>FSM</sub>	7840				Α
I <sup>2</sup> t value for fusing	T <sub>j</sub> =25℃	l <sup>2</sup> t	307000				A <sup>2</sup> s
Insulation voltage	A.C 50Hz(1s/1min)	Viso	3600/3000			V	

## **Electrical Characteristics** (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25°C)

Parameter	Test Conditions	Symbol	Values	Unit
Peak forward voltage	I <sub>F</sub> =720A, t <sub>P</sub> =380µs	V <sub>FM</sub>	≤1.6	٧
Threshold voltage	Tj=150℃	V <sub>TO</sub>	≤0.78	V
Dynamic resistance	T <sub>j</sub> =150℃	Rd	≤1.05	mΩ
	V <sub>R</sub> =V <sub>RRM</sub>			
Repetitive peak reverse current	T <sub>j</sub> =25℃	I <sub>RRM1</sub>	≤100	μA
, .	T <sub>j</sub> =150℃	I <sub>RRM2</sub>	≤90	mA
Thermal resistance(Per chip)	Junction to case	R <sub>th(j-c)</sub>	0.13	°C/W
	Case to heatsink	R <sub>th(c-s)</sub>	0.05	



### **Mechanical Characteristics**

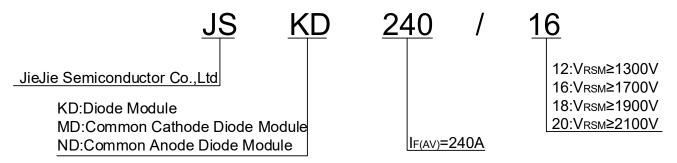
Module size	94mm×34.3mm			
Module height	30.3mm			
Terminal distance of (1) /(2) /(3)	23mm			
Mounting torque(M5)	5±15%Nm			
Terminal torque(M6)	5±15%Nm			
916°92 94.05 94.05 2.864.03 9.105 2.864.03 72 80.02	(1)			

#### **Instructions and Precautions**

- 1) There is no severe vibration and shock in operating environment, and there should be no impurity and atmosphere which may corrode metal and damage the insulation in the air-dielectric.
- 2) The operating condition of the product can't out of range of the above parameters.
- 3) When the product is installed on the radiator, the radiator's surface should be confirmed flat, smooth, wipe clean with alcohol, and coated evenly with a layer of thermal grease which thickness is moderate on the contact surface between product and radiator. When the module is fastened on the surface of the radiator, the M5 or M6 screws and spring washers are used and fastened with 5NM torque. After the module is operated 1 hour, all screws must be refastened.
- 4) The connection with the main electrode of module can use copper, welding, socket and so on. The contact surface should be smooth and flat, which make good contact. While the connection with the control electrode of module is installed, attention should be paid to the corresponding connection of each pin. After the completion of the connection, do not plug and pull out the lead of the control electrode freely.



## **Ordering Information**



### **Performance Curves**

FIG.1: Forward characteristics(per diode)



FIG.3: Forward current vs. case temperature

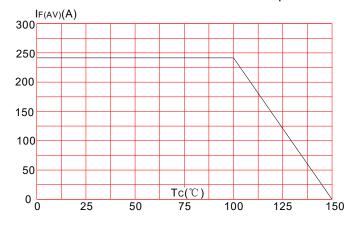


FIG.2: Peak on-state surge current

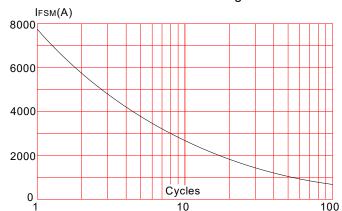
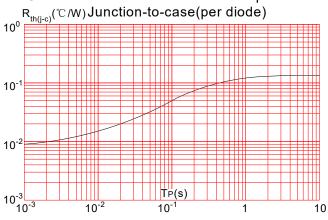


FIG.4: Maximum transient thermal impedance





Information furnished in this document is believed to be accurate and reliable. However, JieJie Semiconductor Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

This document is the second version which is made in 29-Aug.-2024. This document supersedes and replaces all information previously supplied.

is registered trademarks of JieJie Semiconductor Co., Ltd. Copyright©2024 Semiconductor Co., Ltd. Printed All rights reserved.